## PATENTS ABSTRACTS OF JAPAN

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> (54) SEMICONDUCTOR DEVICE FOR CHIP-ON-CHIP (11)

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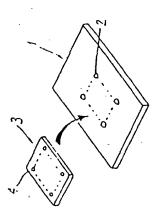
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(71) HITACHI LTD (72) JOJI OKADA

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applying the other chip including an E'PROM element on one chip including a semiconductor element necessary for a wafer process different from the E' PURPOSE: To easily realize an all in one device and to simplify a process by PROM element by a face-down bonding.

element necessary for a different wafer process and other elements adhere by a protruding electrode 2 formed on chips including the element on a base chip I with the chip including the latter element as a base by a chip-on-chip and since the chip may be placed on the chip, a process is simplified, and the by face down bonding. Thus, satisfactory products can be selected as chips, functions are unified on the same wafer by an all-in-one process, an E<sup>2</sup>PROM CONSTITUTION: In order to manufacture an all-in-one device in which all chip size can be reduced



zu den Bibliographicdaten LEGENDE

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